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Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete If Known</b>			
		Application Number	10/562,071		
		Filing Date	December 22, 2005		
		First Named Inventor	Yoshitaka SUGAWARA		
		Art Unit	2815		
		Examiner Name	Ho Anthony		
Sheet	2	of	2	Attorney Docket Number	102253.67240US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/A.H./	C1	European Search Report dated August 16, 2007 (Three (3) pages).	

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PTO/SE/88a (08-03)

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(use as many sheets as necessary)*

Sheet

1

of

2

**Complete if Known**

Application Number

(To Be Assigned)

Filing Date

**December 22, 2009**

First Named Inventor

**Yoshitaka SUGAWARA**

### Art Unit

**(To Be Assigned)**

Examiner Name \_\_\_\_\_

**(To Be Assigned)**

Attorney Docket Number

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U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

**Examiner  
Signature**

/Anthony Ho/

Date \_\_\_\_\_

**Considered**

12/17/2007

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			Filing Date	December 22, 2005	
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/A.H./	AF	Y. SUGAWARA, ET AL., "12-19kV 4H-SiC Pin Diodes With Low Power Loss", Proceedings of 2001 International Symposium on Power Semiconductor Devices & IC's, (2001) pp. 27-30	
/A.H./	AG	International Search Report Dated November 02, 2004 W/ Partial English Translation (Three (3) Pages)	

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